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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/798,719	03/11/2004	Christian Herzum	1890-0066	5006
7590 11/16/2005				
Maginot, Moore & Beck Suite 3000 111 Monument Circle Indianapolis, IN 46204		EXAMINER PERALTA, GINETTE		
		ART UNIT 2814		PAPER NUMBER

DATE MAILED: 11/16/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

<b>Office Action Summary</b>	Application No. 10/798,719	Applicant(s) HERZUM ET AL.	
	Examiner Ginette Peralta	Art Unit 2814	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --  
**Period for Reply**

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

#### Status

- 1) ☒ Responsive to communication(s) filed on 24 August 2005.  
 2a) ☒ This action is **FINAL**.                      2b) ☐ This action is non-final.  
 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

#### Disposition of Claims

- 4) ☒ Claim(s) 14-21 is/are pending in the application.  
     4a) Of the above claim(s) \_\_\_\_\_ is/are withdrawn from consideration.  
 5) ☐ Claim(s) \_\_\_\_\_ is/are allowed.  
 6) ☒ Claim(s) 14-21 is/are rejected.  
 7) ☐ Claim(s) \_\_\_\_\_ is/are objected to.  
 8) ☐ Claim(s) \_\_\_\_\_ are subject to restriction and/or election requirement.

#### Application Papers

- 9) ☐ The specification is objected to by the Examiner.  
 10) ☐ The drawing(s) filed on \_\_\_\_\_ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.  
     Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).  
     Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).  
 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

#### Priority under 35 U.S.C. § 119

- 12) ☒ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  
     a) ☐ All    b) ☐ Some \* c) ☒ None of:  
         1. ☒ Certified copies of the priority documents have been received.  
         2. ☐ Certified copies of the priority documents have been received in Application No. \_\_\_\_\_.  
         3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).  
     \* See the attached detailed Office action for a list of the certified copies not received.

#### Attachment(s)

- |  |   |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892)  | 4) <input type="checkbox"/> Interview Summary (PTO-413)<br>Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948)                                   | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152)             |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)<br>Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____  |

DETAILED ACTION

*Claim Rejections - 35 USC § 103*

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 14-21 are rejected under 35 U.S.C. 103(a) as being unpatentable over Ozaki et al. in view of Yoo (U. S. Pat. 5,573,980).

Regarding claim 14, Ozaki et al. discloses in Fig. 2L, a semiconductor device comprising a substrate 1; active areas formed within the substrate 1 comprising a source area and a drain area (8a and 8b); a gate 4 disposed between the source area and the drain area and insulated from the substrate by an oxide layer 3; a first non-planar metallization level (9 and 19b) formed on the substrate in contact with the active areas, including a first portion connected to the source area, a second portion connected to the drain area, and a third portion at least partially covering the gate, the third portion including a portion covering a side face of the gate facing the source area, a portion covering a surface of the gate facing away from the substrate; and a second planar metallization level 50 arranged spaced apart from the first metallization level above the

substrate and connected to the second portion of the first metallization level *19b* via a through connection *14b*.

Ozaki discloses the claimed invention with the exception of the first non-planar metallization level having a third portion that includes a portion covering a part of a side face of the gate facing the drain area, the portion covering the side face of the gate facing the drain area terminating at an end displaced from the substrate by a predetermined displacement.

Yoo discloses a semiconductor device that comprises a substrate *10*; active areas formed within the substrate *10* comprising a source *22* and a drain area *20*; a gate *28* disposed between the source area and the drain area and insulated from the substrate by an oxide layer *16*; a first non-planar metallization level formed on the substrate in contact with the active areas including a first portion connected to the source area *22*, a second portion connected to the drain area *20* and a third portion at least partially covering the gate, the third portion including a portion covering a side face of the gate facing the source area, a portion covering a surface of the gate facing away from the substrate; and a portion covering a part of the side face of the gate facing the drain area, the portion covering the side face of the gate facing the drain area terminating at an end displaced from the substrate by a predetermined displacement, as shown in Fig. 6; wherein the third portion covers the side face of the gate facing the drain area and terminates at an end displaced from the substrate by a predetermined displacement for

the disclosed intended purpose of forming a low resistance interconnection to other areas of the substrate.

Thus, it would have been obvious to one of ordinary skill in the art at the time the invention was made to form a first level metallization having a third portion at least partially covering the gate, the third portion including a portion covering a side face of the gate facing the source area, a portion covering a surface of the gate facing away from the substrate; and a portion covering a part of the side face of the gate facing the drain area, the portion covering the side face of the gate facing the drain area terminating at an end displaced from the substrate by a predetermined displacement, as shown in Fig. 6; wherein the third portion covers the side face of the gate facing the drain area and terminates at an end displaced from the substrate by a predetermined displacement for the well known and disclosed intended purpose of forming a low resistance interconnection to other areas of the substrate.

Regarding claim 15, Ozaki et al. discloses in Fig. 2L that the first portion 9 and the third portion 9 of the first non-planar metallization level are connected.

Regarding claim 16, Ozaki et al. discloses in Fig. 2L that between the first non-planar metallization level (9 and 19b) and the second planar metallization level 50 an insulating layer 12 is arranged, wherein in the insulating layer 12 at least one through connection 14b for a connection of the first non-planar metallization level to the second planar metallization level is formed.

Regarding claim 17, the structure of Ozaki et al. including a third portion of a first non-planar metallization level connected to the drain region inherently results in the shielding of the gate against electrostatic or electrodynamic interferences.

Regarding claim 18, Ozaki et al. discloses the claimed invention with the exception disclosing the distance between the third portion and the second portion and the displacement, but it discloses that the second portion is in relative close distance to the third portion.

It would have been obvious to one of ordinary skill in the art at the time the invention was made to vary the distance between the third and second portions of the metallization level as there is no statement denoting the criticality of the distance between the third and second portions, and as long that the two portions do not coincide therefore affecting the operation of the device.

"In the case where the claimed ranges "overlap or lie inside ranges disclosed by the prior art" a prima facie case of obviousness exists. *In re Wertheim*, 541 F.2d 257, 191 USPQ 90 (CCPA 1976); *In re Woodruff*, 919 F.2d 1575, 16 USPQ2d 1934 (Fed. Cir. 1990) (The prior art taught carbon monoxide concentrations of "about 1-5%" while the claim was limited to "more than 5%." The court held that "about 1-5%" allowed for concentrations slightly above 5% thus the ranges overlapped.)" (MPEP 2144.04)

Regarding claim 19, Ozaki et al. discloses in Fig. 2L that the device further comprises an oxide layer 5.

Regarding claim 20, Ozaki et al. discloses in Fig. 2L that the structure further comprises a reduced surface field area 6b formed in the substrate and disposed between the gate 4 and the drain area 8b.

Regarding claim 21, Ozaki et al. as modified by Yoo discloses that the predetermined displacement is set to be less than the thickness of the gate.

*Response to Arguments*

3. Applicant's arguments with respect to claims 14-21 have been considered but are moot in view of the new ground(s) of rejection.

*Conclusion*

4. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ginette Peralta whose telephone number is (571) 272-1713. The examiner can normally be reached on Monday to Friday 8:00 AM- 5:30 PM.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Wael Fahmy can be reached on (571) 272-1705. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GP

*Wael Fahmy*  
SPC 2814